PTO/SB/08A (07-05)

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Sheet 1

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Complete if Known					
Application Number	10/074,174				
Filing Date	02/10/02				
First Named Inventor	Haw-minn Lu				
Art Unit	2668				
Examiner Name	Hanh N. Nguyen				
Attorney Docket Number					

	U. S. PATENT DOCUMENTS							
Examiner Initials*	Cite No.1	Document Number Number-Kind Code ^{2 (F Arrows)}	Publication Date MM-DD-YYYY		Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
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	FORE	IGN PATENT DOCU	MENTS		
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considered. Include copy of this/form with next communication to applicant. Applicant's unique citation de	esignation numi	ber (optional). ² See Kinds Code	e of
USPTO Patent Documents at www.uspto.gov or MPEP 901.04. 3 Enter Office that issued the document,	by the two-lett	er code (Wi	PO Standard ST.3).	4 For
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Translation is attached.

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Sheet

	Complete if Known	
Application Number	10/074,174	
Filing Date	02/10/02	
First Named Inventor	Haw-minn Lu	
Art Unit	2113 2668	
Examiner Name	Rebert W. Beausollel, Jr	NAME

NON PATENT LITERATURE DOCUMENTS Cite Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of Examiner No.1 the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue Initials' number(s), publisher, city and/or country where published. AGRAWAL, "Testing and Fault-Tolerance of Multistage Interconnection Networks," Computer, Apr. 1982, pp. 41-53, vol. 15, no. 4, IEEE, US BHUYAN, et. al "Design and Performance of Generalized Interconnection Networks." IEEE Transactions on Computers, Dec. 1983, pp. 1081-1090, vol. 32, no. 12, IEEE, US BLAKE, et. al "Multistage Interconnection Network Reliability," IEEE Transactions on Computers, Nov. 1989, pp. 1600-1603, vol. 38, no. 11, IEEE, US CHIN, et. al "Packet Switching Networks for Multiprocessors and Data Flow Computers," IEEE Transactions on Computers, Nov. 1984, pp. 991-1003, vol. 33, no. 11, IEEE, US KUMAR, et. al "Failure Dependent Performance Analysis of a Fault-Tolerant Multistage Interconnection Network," IEEE Transactions on Computers, Dec. 1989, pp. 1703-1713, vol. 38, no. 12, IEEE, US TZENG, et. al "Realizing Fault-Tolerant Interconnection Network via Chaining," IEEE Transactions on Computers, Apr. 1988, pp. 458-462, vol. 37, no. 4, IEEE, US VARMA, et. al "Fault-Tolerant Routing in Multistage Interconnection Networks," IEEE Transactions on Computers, Mar. 1989, pp. 385-393, vol. 38, no. 3, IEEE, US

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PTO/SB/08B (08-03)

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		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
HN		CIZEK, et al. "Tradeoff Between Cost and Reliability in Packet Switching MultiStage Interconnection Networks," AFRICON '92 Proceedings., 3rd AFRICON Conference, 22-24 Sept. 1992, pp. 365-368, IEEE, South Africa (Reprinted US).	

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PTO/SB/08A (02-03)

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Filing Date	02/10/02					
First Named Inventor	Haw-minn Lu					
Art Unit	2113					
Evaminer Name	Report M. Regusolial Jr i C.					

Attorney Docket Number

			U. S. PATENT	DOCUMENTS	
Examiner Initials*	Cite No.1	Document Number Number-Kind Code ^{2 (f known)}	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages; Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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Examiner Signature Date Considered 2/16/06

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